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AMENDMENT TRANSMITTAL LETTER			Docket No. 08228/019001
Application No. 10/044,686-Conf. #9706	Filing Date January 11, 2002	Examiner F. C. Hiteshew	Art Unit 1765
Applicant(s): Shiro Sakai et al.			
Invention: NITRIDE SEMICONDUCTOR CHIP AND METHOD FOR MANUFACTURING NITRIDE SEMICONDUCTOR CHIP			

TO THE COMMISSIONER FOR PATENTS

Transmitted herewith is an amendment in the above-identified application.
The fee has been calculated and is transmitted as shown below.

CLAIMS AS AMENDED					
	Claims Remaining After Amendment	Highest Number Previously Paid	Number Extra Claims Present	Rate	
Total Claims	11	- 20 =		x	
Independent Claims	2	- 3 =		x	
Multiple Dependent Claims (check if applicable) <input type="checkbox"/>					
Other fee (please specify):					
TOTAL ADDITIONAL FEE FOR THIS AMENDMENT:					

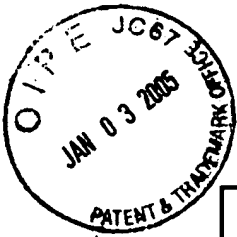
- ☐ Large Entity
- ☒ Small Entity
- ☐ No additional fee is required for this amendment.
- ☐ Please charge Deposit Account No. _____ in the amount of \$ _____.
A duplicate copy of this sheet is enclosed.
- ☐ A check in the amount of \$ _____ to cover the filing fee is enclosed.
- ☐ Payment by credit card. Form PTO-2038 is attached.
- ☒ The Director is hereby authorized to charge and credit Deposit Account No. 50-0591
as described below. A duplicate copy of this sheet is enclosed.
- ☒ Credit any overpayment.
- ☒ Charge any additional filing or application processing fees required under 37 CFR 1.16 and 1.17.

Jonathan P. Osha
Jonathan P. Osha
Attorney Reg. No.: 33,986

Dated: January 3, 2005

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I hereby certify that this correspondence is being deposited with the U.S. Postal Service as Express Mail, Airbill No. EV535680231US, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date shown below.	
Dated: January 3, 2005	Signature: <u>Yuki Tsukuda</u> (Yuki Tsukuda)



Application No. (if known): 10/044,686

Attorney Docket No.: 08228/019001

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Docket No.: 08228/019001
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Shiro Sakai et al.

Confirmation No. 9706

Application No.: 10/044,686

Art Unit: 1765

Filed: January 11, 2002

Examiner: F. C. Hiteshew

For: NITRIDE SEMICONDUCTOR CHIP AND
METHOD FOR MANUFACTURING NITRIDE
SEMICONDUCTOR CHIP

REPLY UNDER 37 C.F.R. § 1.111

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

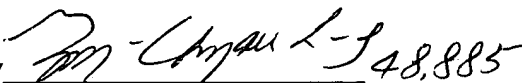
In response to the Restriction Requirement dated December 2, 2004, Applicant elects Group I, which corresponds to claims 1-6. Accordingly, claims 1-6 are elected for continued prosecution without traverse.

Applicant thanks the Examiner for noting that pending U.S. patent application office actions cannot be submitted as prior art.

Applicant believes this reply is fully responsive to all outstanding issues. If this belief is incorrect, or other issues arise, the Examiner is encouraged to contact the undersigned or his associates at the telephone number listed below. Please apply any charges not covered, or any credits, to Deposit Account 50-0591 (Reference Number 08228/019001).

Dated: January 3, 2005

Respectfully submitted,

By 
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